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February 5, 2007

TO: Commissioner for Patents
P.O. Box 1450, Alexandria, VA 22313-1450
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Applicant : Leland R. Nevill

Attorney Docket No.: 500060.02

Serial No. : 09/998,594

Group Art Unit : 2876

Filed : November 16, 2001

Examiner : Daniel St. Cyr

Title : METHOD AND APPARATUS FOR IDENTIFYING INTEGRATED CIRCUITS

Date Sent : February 5, 2007

Date Due : n/a

Pursuant to your request, attached is the response with changes to claim 23. Applicant requests the same be made of record.

Documents Attached for Filing:

	<u>No. Pages</u>
Fee Transmittal Sheet (+ copy)	2
Second Supplemental Response	10
Supplemental Declaration	1

Total Number of Pages (including cover sheet): 14**Originals will not be sent**

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